

Substitute for form 1449A/PTO		Application Number	10716,596
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>		Filing Date	November 20, 2003
		First Named Inventor	Michael Farnwald
		Art Unit	2111
		Examiner Name	Glenn Allen Auve
Sheet	5 of 18	Attorney Doctor Number	57941.000063

**U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
GA	93.	US- 4,933,835	06-12-1990	Sachs, et al.	
	94.	US- 4,937,733	06-26-1990	Gillett, Jr., et al.	
	95.	US- 4,939,510	07-03-1990	Masheff, et al.	
	96.	US- 4,940,909	07-10-1990	Mulder, et al.	
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	98.	US- 4,945,516	07-31-1990	Kashiyama	
	99.	US- 4,947,484	08-07-1990	Twitty, et al.	
	100.	US- 4,949,301	08-14-1990	Joshi, et al.	
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	102.	US- 4,965,792	10-23-1990	Yano	
	103.	US- 4,975,763	12-04-1990	Baudouin, et al.	
	104.	US- 4,982,400	01-01-1991	Ebersole	
	105.	US- 4,998,069	03-05-1991	Nguyen, et al.	
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	107.	US- 5,012,408	04-30-1991	Conroy	
	108.	US- 5,023,488	06-11-1991	Gunning	
	109.	US- 5,034,964	07-23-1991	Khan, et al.	
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PTO-1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT		ATTY. DOCKET NO. 57941.000063	SERIAL NUMBER 10/716,596
		APPLICANT(S) MICHAEL FARMWALD ET AL.	
		FILING DATE November 20, 2003	GROUP ART UNIT Unassigned 2111

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	148.	Robert J. Lodi et al., "Chip and System Characteristics of a 2048-Bit MNOS-BORAM LSI Circuit," IEEE International Solid-State Circuits Conference, (Feb. 1976)
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↓	9.	Riordan T. "MIPS R2000 Processor Interface 78-00005(C)", MIPS Computer Systems, Sunnyvale, CA, June 30, 1987, pp 1-83
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